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Debug automation for electrical faults (delay faults) finds the potentially failing speedpaths in a circuit at gate-level. The various debug approaches described achieve high diagnosis accuracy and reduce the debugging time, shortening the IC development cycle and increasing the productivity of designers.

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Mehdi Dehbashi G rschwin Fey Debug Automation from Pre-Silicon to Post-Silicon 123 Mehdi Dehbashi Institute of Computer Science University of Bremen Bremen, Germany G rschwin Fey Institute of Space Systems German Aerospace Center Bremen, Germany ISBN 978-3-319-09308-6 ISBN 978-3-319

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Therefore, debug automation procedures are required to accelerate nding and xing bugs and faults and, consequently, to increase the productivity of IC design.

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Describes a unified framework for debug automation that is used at both pre-silicon and post-silicon stages Provides approaches for debug automation of a hardware system at different levels of abstraction, i.e., chip, gate-level, RTL, and transaction level Includes techniques for debug automation of

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Automating Postsilicon Debugging and Repair
ronic design automation tools and algorithms address this 2problem. Postsilicon debugging is becoming more important because actual chips cannot be simulated presilicon with sufficient accuracy. Design deficiencies can involve com-plex thermal and inductive effects, while new semicon-ductor fabrication technologies can cause manufactur-ing glitches due to unexpected light-diffraction patterns

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The automated debug approach for design bugs finds the potential fault candidates at RTL and gate-level of a circuit. Debug techniques for logic bugs and synchronization bugs are demonstrated, enabling readers to localize the most difficult bugs. Debug automation for electrical faults (delay faults) finds the potentially failing speedpaths in a circuit at gate-level. The various debug

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